

**APPLICATION FOR UNITED STATES PATENT**

UNITED STATES PATENT AND TRADEMARK OFFICE

**Honeywell Case No. B10-17363DIV**

(MBHB Case No. 00-280-A)

Title: **CONTACTLESS ACCELERATION SWITCH**

Inventor: Joon-Won Kang, a resident of Redmond, Washington, and a citizen of Republic of Korea.

Assignee: Honeywell International Inc.  
101 Columbia Road  
P.O. Box 2245  
Morristown, NJ 07962-2245

## **Contactless Acceleration Switch**

### FIELD

The present invention relates generally to acceleration switches, and more particularly, relates to a semiconductor acceleration switch without metal contacts.

### BACKGROUND

Acceleration switches are designed to switch on or off when a threshold acceleration value of acceleration is detected. An example of an application using acceleration switches is air bag systems. The acceleration switch in an airbag system will detect a sudden de-acceleration of the vehicle. When the threshold acceleration value is reached, the contacts on the switch close, sending a signal to the control module. Acceleration switches are also used in free fall detection systems for elevators, seat belt sensors, and machine monitoring of excess vibrations.

Acceleration switches were initially mechanical switches that included a spring loaded mass. Today, acceleration switches are more commonly manufactured using micromachining. Micromachining typically involves combining electronics and tiny mechanical components on a semiconductor chip. Initial designs used bulk micromachining, which consists of making micromechanical devices by etching into the silicon wafer. Bulk micromachining makes extensive use of wafer bonding, which is the process of permanently joining different silicon wafers together.

Further improvements in the manufacture of acceleration switches have been

made using surface micromachining. Surface micromachining fabricates micromechanical devices on the surface of a silicon wafer. The features of the device are built up layer by layer through a combination of deposition, patterning, and etching stages. This technique is compatible with other semiconductor processing that may be performed on the same wafer for other purposes. Results from surface micromachining may be more uniform and more repeatable than those obtained from bulk micromachining.

Typical acceleration switches have contained at least one electrical contact. The contacts are designed to close when the threshold acceleration value is detected. The contacts are formed with a metal, such as gold. However, there may be problems with the use of metal contacts, such as microwelding, arcing, and oxidation. These problems may contribute to the failure of the switch.

It would be desirable to have an acceleration switch that does not employ a metal contact to signal when the threshold acceleration value is detected.

SUMMARY

In accordance with this invention, a contactless acceleration switch contains a substrate layer containing a source, a drain, a threshold adjustment channel, at least two insulator posts, a mass, a spring, and a gate insulating layer. The source, the drain, the threshold adjustment channel, and the gate insulating layer are located between the at least two insulator posts. The spring is attached to each of the at least two insulator posts and supports the mass, above the substrate layer.

The contactless acceleration switch is made as follows. Implant the source, the drain, and the threshold adjustment channel in the substrate layer such that the threshold adjustment channel is located between the source and the drain. Form the at least two insulator posts on the substrate layer such that the source, the drain, and the threshold adjustment channel are located between the at least two insulator posts. Form a first sacrificial layer on the substrate layer between the at least two insulator posts. Form the mass on the first sacrificial layer. Form a second sacrificial layer shaped to provide a pattern for forming the spring. Form the spring. Remove the first sacrificial layer and the second sacrificial layer. Form the gate insulating layer.

BRIEF DESCRIPTION OF THE DRAWINGS

Preferred embodiments are described below in conjunction with the appended drawing figures, wherein like reference numerals refer to like elements in the various figures, and wherein:

5        Fig. 1 is a cross-sectional view of a contactless acceleration switch according to an exemplary embodiment;

      Figs. 2-5 are cross-sectional views of the contactless acceleration switch of Fig. 1 at various stages of fabrication according to an exemplary embodiment; and

      Fig. 6 is a flow chart of the fabrication process of a contactless acceleration switch  
10    according to an exemplary embodiment.

### DETAILED DESCRIPTION

Fig. 1 is a cross sectional view of a contactless acceleration switch 100 according to an exemplary embodiment. The figures are not drawn to scale and are approximations of an exemplary embodiment. For example, corners may be rounded in an exemplary  
5 embodiment, rather than straight as depicted. Contactless acceleration switch 100 includes a substrate layer 102, a source 104, a drain 106, a threshold adjustment channel 108, at least two insulator posts 110, a mass 112, a spring 114, and a gate insulating layer 120.

The substrate layer 102 is the underlying material upon which the contactless  
10 acceleration switch 100 is fabricated. The substrate layer 102 may be composed of a semiconductor material such as silicon or gallium arsenide. Silicon is the preferred material for the substrate layer 102 in an exemplary embodiment. The substrate layer 102 may be doped with either a P-type or N-type dopant.

The source 104 and the drain 106 may be substantially located in the substrate  
15 layer 102. The source 104 and the drain 106 may be doped based on whether the substrate layer 102 has been doped. Typically the source 104 and the drain 106 are doped with the opposite dopant type as the substrate layer 102. For example, if the substrate layer 102 has been doped with a P-type dopant, the source 104 and the drain 106 may be doped with an N-type dopant. Ion implantation may be employed to dope  
20 the source 104 and the drain 106; however, other methods may be employed.

The threshold adjustment channel 108 may be located between the source 104 and the drain 106, substantially within the substrate layer 102. The threshold adjustment

channel 108 may be doped to a level that may cause the threshold adjustment channel 108 to invert when the mass 112 moves substantially towards the substrate layer 102. Ion implantation may be employed to dope the threshold adjustment channel 108; however, other methods may be suitable for this purpose. The threshold adjustment channel 108  
5 may be doped with either P-type or N-type dopant. In an exemplary embodiment, the threshold adjustment channel 108 may be doped with the opposite dopant type as the source 104 and the drain 106. However, some embodiments will use the same dopant type for the threshold adjustment channel 108, the source 104, and the drain 106.

The gate insulating layer 120 may be located substantially above the source 104,  
10 the drain 106, and the threshold adjustment channel 108. The gate insulating layer 120 may substantially limit electric conduction between the mass 112 and the substrate layer 102 when the mass 112 moves substantially towards the substrate layer 102. The gate insulating layer 120 may be composed of an insulating material, such as silicon dioxide or silicon nitride. Silicon dioxide is the preferred material for the gate insulating layer  
15 120 in an exemplary embodiment.

The at least two insulator posts 110 may be composed of an insulating material, such as silicon dioxide or silicon nitride. In an exemplary embodiment, the at least two insulator posts 110 are composed of silicon dioxide. The at least two insulator posts may be substantially located on the substrate layer 102. The source 104, the drain 106, the  
20 threshold adjustment channel 108, and the gate insulating layer 120 may be substantially located between the at least two insulator posts 110. The at least two insulator posts may provide an interface between the substrate layer 102 and the spring 114.

The mass 112 may be formed with an electrically conductive material, such as metal or doped silicon. In an exemplary embodiment, the mass 112 is composed of doped silicon. The mass 112 may operate as a moveable gate in combination with the source 104, and the drain 106, forming a field effect transistor (FET). The mass 112  
5 may be located substantially above the source 104, the drain 106, the threshold adjustment channel 108, and the gate insulating layer 120.

A substantially constant voltage may be applied between the mass 112 and the substrate layer 102. The voltage may be determined using factors such as mass size, spring constant, operation range, and hysteresis. The voltage may range from less than  
10 five volts for low acceleration range devices to hundreds of volts for large acceleration range devices. The voltage amount may also provide a method of adjusting a threshold acceleration value. The threshold acceleration value may be the value of acceleration at which the contactless acceleration switch 100 may be triggered.

An air gap may be located between the mass 112 and the gate insulating layer 120  
15 when an acceleration level is below the threshold acceleration value. The thickness of the air gap may be dependant upon the value of the substantially constant voltage applied between the mass 112 and the substrate 102, and the threshold acceleration value. The air gap thickness for a low range value of acceleration that may trigger the contactless acceleration switch 100 may be greater than 1000 Angstroms.

20 The spring 114 may be formed with an electrically conductive material, such as metal or doped silicon. In an exemplary embodiment, the spring 114 is composed of doped silicon. The spring 114 may be attached to each of the at least two insulator posts



110. The mass 112 may be attached to substantially the center of the spring 114.

Below the threshold acceleration value, the mass 112 may not be attracted to the substrate layer 102 and the threshold adjustment channel 108 may not be inverted. When the acceleration level exceeds the threshold acceleration value, an electric field  
5 between the mass 112 and the substrate layer 102 may form. The electric field may create an electrostatic force, which may attract the mass 112 to the substrate layer 102. The mass 112 may move towards the substrate layer 102 to a position of critical distance. The position of critical distance may be the distance between the mass 112 and the substrate layer 102 at which point the electrostatic force exceeds a spring force. The  
10 spring force may be a force of the spring 114 that operates to maintain the air gap between the mass 112 and the substrate layer 102.

Once the mass 112 reaches the position of critical distance, the mass 112 may suddenly contact the gate insulating layer 120, eliminating the air gap. In other words, some or all of the mass 112 may contact at least a portion of the gate insulating layer 120.  
15 When the mass 112 substantially contacts the gate insulating layer 120, the strength of the electric field may be at a maximum level. The threshold adjustment channel 108 may invert, allowing current to flow between the source 104 and the drain 106, which may turn on the FET. The threshold acceleration value may be determined by the initial air gap thickness, the position of critical distance, the spring strength, and the value of the  
20 substantially constant voltage applied between the mass 112 and the substrate layer 102.

The source 104 and the drain 106 may act as electrodes, providing an electrical signal indicating that the threshold acceleration value has been reached. The electrical

signal may be sent to a controller for further processing. The electrical signal may be a value of voltage, current, or resistance. As described, the contactless acceleration switch 100 may substantially provide on-off switching without the need for metal contacts.

Fig. 2 is a cross sectional view of the contactless acceleration switch 100 during the initial steps of processing according to an exemplary embodiment. The contactless acceleration switch 100 may be fabricated using surface micromachining technology. Manufacturing of the contactless acceleration switch 100 may begin by performing the source 104, drain 106, and threshold adjustment channel 108 implants. Ion implantation may be used to dope the source 104, the drain 106, and the threshold adjustment channel 108 regions in the substrate layer 102. Other doping methods that are compatible with the substrate layer 102 may also be used. As previously discussed, the substrate layer 102, the source 104, the drain 106, and the threshold adjustment channel 108 may be doped with either P-type or N-type dopant. In an exemplary embodiment, the substrate layer 102 and the threshold adjustment channel 108 are doped with the opposite dopant type as the source 104 and the drain 106. However, other embodiments may use a different selection of dopant types.

Fig. 3 is a cross sectional view of the contactless acceleration switch 100 during additional stages of processing according to an exemplary embodiment. The at least two insulator posts 110 are installed. An insulating material, such as silicon dioxide or silicon nitride, may be deposited on the substrate layer 102. In an exemplary embodiment silicon dioxide is used. The at least two insulator posts 110 are thermally grown in an exemplary embodiment. However, the at least two insulator posts 110 may also be

deposited using low temperature oxidation, low pressure chemical vapor deposition, sputtering, or other deposition techniques. A photoresist may be placed on the insulating layer to define the location of the at least two insulator posts 110. In an exemplary embodiment, the at least two insulator posts 110 are then formed using a wet etch technique. Other etching techniques, such as plasma etching, may also be suitable. The photoresist may then be removed.

Fig. 4 is a cross sectional view of the contactless acceleration switch 100 during additional stages of processing according to an exemplary embodiment. After the at least two insulator posts 110 are formed, a first sacrificial layer 116 may be deposited. The first sacrificial layer 116 may be composed of silicon dioxide, polyimide, photoresist, various polymers, doped silicon, or metals. The selection of the first sacrificial layer material may depend upon the material selected for the mass 112 and the spring 114, and which etching technique may be employed. In an exemplary embodiment, the first sacrificial layer 116 is composed of silicon dioxide.

The first sacrificial layer 116 may be deposited substantially between the at least two insulator posts 110. The deposition method selected may be chosen based on the material used for the first sacrificial layer 116. In an exemplary embodiment, the first sacrificial layer 116 is thermally grown. The first sacrificial layer 116 may also be deposited using low temperature oxidation, low pressure chemical vapor deposition, sputtering, or other deposition techniques. The first sacrificial layer 116 is then patterned and etched to form a substantially continuous layer between the at least two insulator posts 110. A wet etch is preferred in an exemplary embodiment; however, other etching

techniques, such as plasma etching, may also be suitable for this purpose.

The mass 112, which operates as a moveable gate, may then be deposited on the first sacrificial layer 116 using a low pressure chemical vapor deposition process. Other deposition methods, such as sputtering, may also be used. The mass 112 is then patterned  
5 and etched. A plasma etch is employed in an exemplary embodiment; however, other etching methods may also be suitable for this purpose.

Fig. 5 is a cross sectional view of the contactless acceleration switch 100 during additional stages of processing according to an exemplary embodiment. A second sacrificial layer 118 may then be deposited. The second sacrificial layer 118 may be  
10 composed of silicon dioxide, polyimide, photoresist, various polymers, doped silicon, or metals. The selection of the second sacrificial layer material may depend upon the material selected for the mass 112 and the spring 114, and which etching technique may be employed. In an exemplary embodiment, the second sacrificial layer 118 is composed of silicon dioxide.

15 The deposition method employed may be chosen based on the material selected for the second sacrificial layer 118. In an exemplary embodiment, low temperature oxidation is employed. However, other deposition techniques, such as tetraethyl orthosilicate, low pressure chemical vapor deposition, sputtering, or other deposition techniques, may also be suitable for this purpose. The second sacrificial layer 118 is then  
20 patterned to provide interconnection between the mass 112 and the at least two insulator posts 110. An etching process is then employed to form the second sacrificial layer 118. In an exemplary embodiment a wet etch is used; however, other etching techniques, such

as plasma etching, may be employed. The shape of the second sacrificial layer may be designed to provide a pattern for forming the spring 114.

The spring material is then deposited substantially on the second sacrificial layer 118 and the at least two insulator posts 110. The deposition process employed may be  
5 chosen to be compatible with the spring material chosen. Low pressure chemical vapor deposition may be used in an exemplary embodiment; however, other deposition techniques, such as sputtering, may also be suitable for this purpose. The spring material may then be patterned and etched to substantially form the spring 114. An exemplary embodiment employs a plasma etch. Other etching techniques, such as a wet etch, may  
10 also be suitable for this purpose.

The first sacrificial layer 116 and the second sacrificial layer 118 may then be substantially removed, leaving the spring 114 and the mass 112 as a free standing structure (see Fig. 1). The first sacrificial layer 116 and the second sacrificial layer 118 may be removed by etching. Any etching process that is selective to the material of the  
15 sacrificial layers may be used. A wet etch may be used in an exemplary embodiment to remove the first sacrificial layer 116 and the second sacrificial layer 118. An air gap may be located substantially between the mass 112 and the substrate layer 102 after the first sacrificial layer 116 is removed.

The gate insulating layer 120 may be deposited on the substrate layer 102,  
20 substantially above the source 104, the drain 106, and the threshold adjustment channel 108. In a preferred embodiment, silicon dioxide may be thermally grown on the substrate layer 102 between the two insulator posts 110 after the first sacrificial layer 116

is removed. In an alternative embodiment, the gate insulating layer 120 may be deposited before the deposition of the first sacrificial layer 116. When the first sacrificial layer 116 is removed, the gate insulating layer 120 may be uncovered. This alternative embodiment may be employed when etching the first sacrificial layer 116 will not damage the underlying gate insulating layer 120. For example, if the first sacrificial layer materials are metal, an etch may be chosen that may remove the first sacrificial layer 116 without damaging the gate insulating layer 120.

Fig. 6 provides a flow chart of fabrication process 600 of the contactless acceleration switch 100 according to an exemplary embodiment. Fabrication process 600 summarizes the process described above with reference to Fig. 2 through Fig. 5. This process may be compatible with complementary metal oxide semiconductor (CMOS) fabrication.

The contactless acceleration switch 100 may also be employed in a manner to detect when the acceleration level goes below the threshold acceleration value. The mass 112 may move away from the substrate layer 102 when the acceleration level goes below the threshold acceleration value. The air gap may be formed between the mass 112 and the substrate layer 102. The electrical signal generated may indicate that the acceleration level is below the threshold acceleration value.

It should be understood that the illustrated embodiments are exemplary only, and should not be taken as limiting the scope of the present invention. For example, a variety of semiconductor fabrication techniques, including various methods of deposition and etching, may be employed without departing from the scope of the invention itself. The

claims should not be read as limited to the described order or elements unless stated to that effect. Therefore, all embodiments that come within the scope and spirit of the following claims and equivalents thereto are claimed as the invention.